

**STATE BOARD OF TECHNICAL EDUCATION AND TRAINING
BOARD OF EXAMINATIONS, CHENNAI 600 025**

TIME TABLE – OCTOBER / NOVEMBER 2011

DIPLOMA IN INFORMATION TECHNOLOGY (K – Scheme)

Date and Day	Time	Subjects as per the Scheme of Examination	Code No
III SEMESTER			
28.10.2011 Friday	9.30 am – 12.30 pm	Operating Systems *	
31.10.2011 Monday	9.30 am – 12.30 pm	C and Data Structures *	
2.11.2011 Wednesday	9.30 am – 12.30 pm	Basics of Electrical and Electronics Engg. *	
IV SEMESTER			
5.11.2011 Saturday	9.30 am – 12.30 pm	Management Information Systems	
8.11.2011 Tuesday	9.30 am – 12.30 pm	Internet Concepts and Web Designing *	
9.11.2011 Wednesday	9.30 am – 12.30 pm	OOPs with Java *	
V SEMESTER			
29.10.2011 Saturday	9.30 am – 12.30 pm	<u>Elective Theory – I</u> (b) Computer Networks and Security *	
1.11.2011 Tuesday	9.30 am – 12.30 pm	Relational Data Base Management Systems *	
3.11.2011 Thursday	9.30 am – 12.30 pm	<u>Elective Theory – I</u> (a) .Net Programming *	
4.11.2011 Friday	9.30 am – 12.30 pm	Open Source Softwares	
VI SEMESTER			
28.10.2011 Friday	2.15 pm – 5.15 pm	Multimedia Systems *	
29.10.2011 Saturday	2.15 pm – 5.15 pm	Software Engineering *	
1.11.2011 Tuesday	2.15 pm – 5.15 pm	<u>Elective Theory – II</u> (a) Financial Accounting and Management	
		(b) Advanced Java Programming	

* Common with Computer Engineering [K - Scheme]

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TIME TABLE – OCTOBER / NOVEMBER 2011

DIPLOMA IN INFORMATION TECHNOLOGY (J – Scheme)

Date and Day	Time	Subjects as per the Scheme of Examination
III SEMESTER		
11.11.2011 Friday	2.15 pm – 5.15 pm	C++ Programming *
IV SEMESTER		
10.11.2011 Thursday	9.30 am – 12.30 pm	Computer Graphics *
V SEMESTER		
31.10.2011 Monday	2.15 pm – 5.15 pm	<u>Elective Theory – I</u> (a) Linux Programming & Administration *
3.11.2011 Thursday	9.30 am – 12.30 pm	(b) Visual C++ Programming *
VI SEMESTER		
10.11.2011 Thursday	2.15 pm – 5.15 pm	<u>Elective Theory – II</u> (a) System Analysis and Design
		(b) Principles of Modern Communication System *
		(c) Principles of Programming Languages
* Common with Computer Engineering [J - Scheme]		